


Electronic Filing System (EFS) Data
Electronic Patent Application Submission
USPTO Use Only

EFS ID:	65192	
Application ID:	10710620	
Title of Invention:	Bandwidth Limited Sampling Circuit of High Linearity	
First Named Inventor:	Ravishankar AYYAGARI	
Domestic/Foreign Application:	Domestic Application	
Filing Date:	2004-07-26	
Effective Receipt Date:	2004-07-26	
Submission Type:	Utility Patent Filing	
Filing Type:	new-utility	
Confirmation number:	4619	
Attorney Docket Number:	TI-36919	
Total Fees Authorized:	1812.0	
Payment Category:	Deposit Account	
Deposit Account Number:	200668	
Deposit Account Name:	Narendra R. Thappeta	
RAM Payment Status:	RAM has not been processed	

Digital Certificate Holder: cn=Narendra R. Thappeta,ou=Registered Attorneys,ou=Patent and
Trademark Office,ou=Department of Commerce,o=U.S. Government,c=US
Certificate Message Digest: cc303e6644a17a92cd30479d3f609cc164f8a84a

PATENT APPLICATION SERIAL NO. _____

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE
FEE RECORD SHEET

07/26/2004 TBESHAH1 00000045 200668 10710620

01 FC:1001	770.00 DA
02 FC:1202	612.00 DA
03 FC:1201	430.00 DA

PTO-1556
(5/87)